Notice of References Cited Application/Control No. 10/662,233 Examiner Art Unit Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,545,513	04-2003	Tsuchida et al.	327/108
	В	US-4,216,393	08-1980	Gillberg et al.	326/88
	С	US-6,127,746	10-2000	Clemente, Stefano	307/131
	D	US-6,459,324	10-2002	Neacsu et al.	327/379
	Ε	US-6,271,708	08-2001	Hoshi et al.	327/377
	F	US-			
	G	US-			
	н	US-			
	1	US-			
	J	US-		, , , , , , , , , , , , , , , , , , ,	
	к	US-			-
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0	,		_		
	Р					
	Q					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	A. Sedra et al., Microelectronic Circuits, 1987, Holt, Reinhart and Winston, pp. 342-345, 408-410.
	٧	
	w	
	x	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.